

**Supplemental Information Disclosure Statement
PTO-1449**

Abstract FR 2555744 A1

A memory chip (P) has its rear face in a groove in the wall of a sample tube. At the rear face is a contact area which consists of a metalised layer (MA) which extends around the tube on its external wall and into the base of the groove. A tape (M) covers the groove and leaves a supplementary contact ring free to which contacts (C0,C1,C2) are made. One contact (C0) has access to the rear face of the chip.

The chip consists of an EPROM which allows it to be reused. It has protected access requiring identification procedures since it contains confidential information regarding the results of the analysis.